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B6J JP J501 J708

(56) Documents Cited

GB 2165992 A

US 5643473 A

US 4529476 A

(58) Field of Search

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INT CL<sup>6</sup> H01L

Online databases: EPODOC, WPI

(54) Abstract Title

**Selective anisotropic plasma etching of a silicon nitride film using CO and a CHF gas at reduced substrate temperature**

(57) A silicon nitride film 13 is preferentially plasma etched with respect to a silicon oxide film 12 and polysilicon 11 using CO together with CHF<sub>3</sub> or H<sub>2</sub> and CHF<sub>3</sub>, CF<sub>4</sub> or C<sub>2</sub>F<sub>6</sub> with the substrate temperature being 10°C or less. O<sub>2</sub>, an inert gas or N<sub>2</sub> may also be present.

FIG. 1

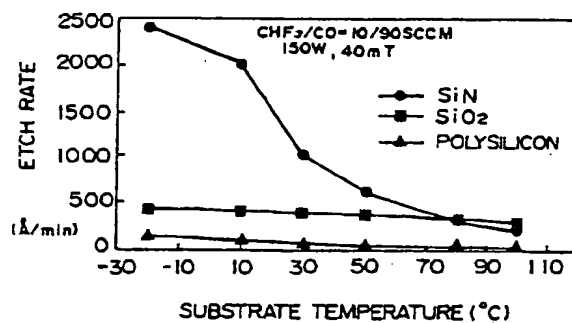
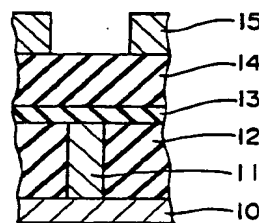


FIG. 3A



etch SiN over Si, polysi  
w/ CHF + CO

GB 2 333 268 A

1/3

FIG. 1

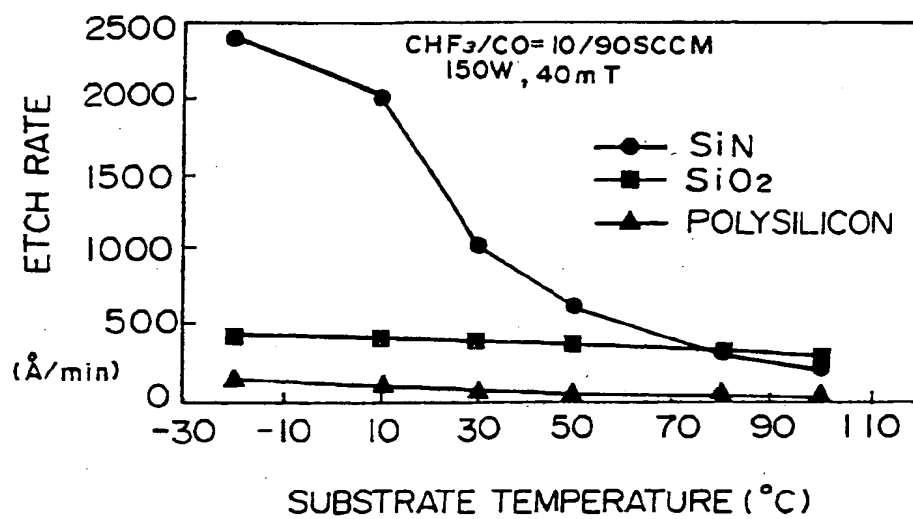


FIG. 2A

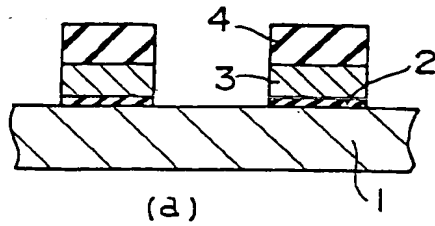


FIG. 2D

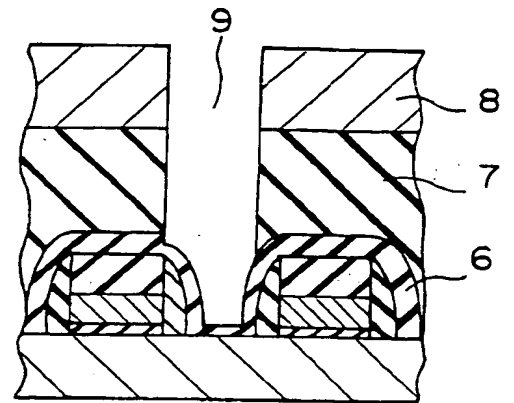


FIG. 2B

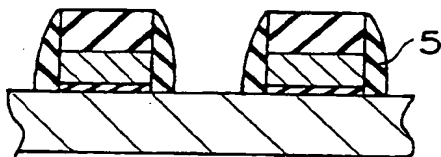


FIG. 2C

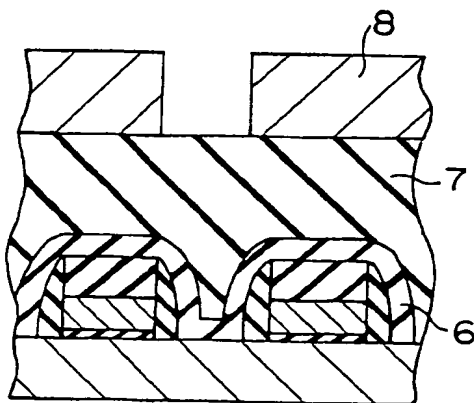
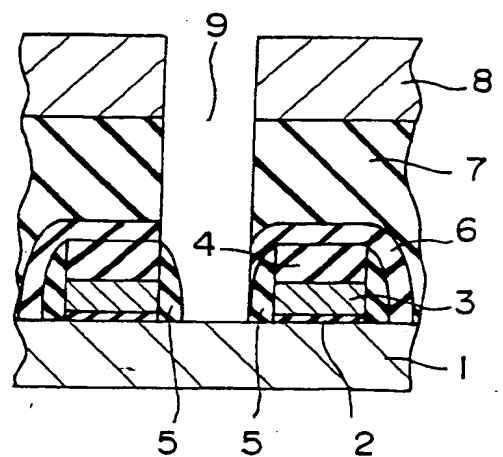


FIG. 2E



3/3

FIG. 3A

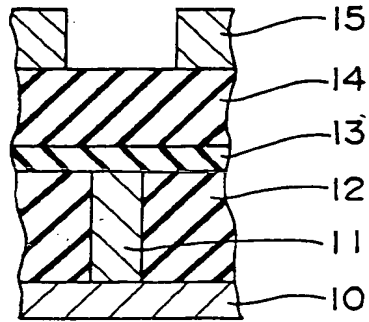


FIG. 3B

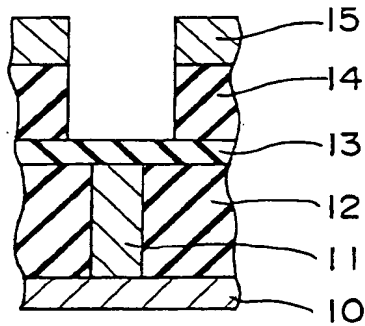
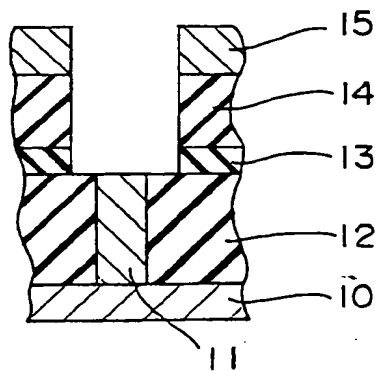


FIG. 3C



# ANISOTROPIC DRY ETCHING METHOD

## BACKGROUND OF THE INVENTION

### 5 Field of the Invention

The present invention relates to an anisotropic dry etching method for selectively anisotropically dry etching a silicon nitride film with respect to a silicon oxide film, a polysilicon film and a silicon film.

### 10 Description of the Related Art

In conventional methods of dry etching a silicon nitride film,  $\text{SF}_6$  gas is used as described in, for example, Japanese Patent Application Laid-Open No. 8-321484 and  $\text{NF}_3$ ,  $\text{Cl}_2$  and the like are used as described in, for example, Japanese Patent Application Laid-Open No. 6-181190. According to those conventional techniques, it is possible to selectively etch a silicon nitride film with respect to a silicon oxide film. However, since a silicon etch rate is fast, it is not possible to selectively etch a silicon nitride film with respect to a silicon film. When using a mixed gas of, for example,  $\text{CHF}_3$  or  $\text{CF}_4$  and  $\text{H}_2$ , the etch rate of silicon can be reduced and the silicon nitride film can be selectively etched with respect to silicon. Use of such a mixed gas, however, results in an increase in the etch rate of the silicon oxide film, as well. It has been, thus, difficult to etch a silicon nitride film having a high etch selectivity with respect to both a silicon film and a silicon oxide film.

With the technical background as stated above,  
Japanese Patent Application Laid-Open Nos. 59-222933 and  
60-115232 disclose the technique of using gas having a F  
to H composition ratio of 2 or less such as  $\text{CH}_2\text{F}_2$  and  $\text{CH}_3\text{F}$   
5 as a method for selectively etching a silicon nitride film  
with respect to a silicon film and a silicon oxide film.  
That is, these references teach that the silicon nitride  
film can be selectively etched with respect to both the  
silicon film and the silicon oxide film by using a gas of  
10 a F to H composition ratio of 2 or less.

The gas having a F to H composition ratio of 2 or  
less, however, is in a range of explosion and, thus, has a  
disadvantage in that it is difficult to handle.

#### SUMMARY OF THE INVENTION

20 An anisotropic dry etching method according to the  
present invention is a method for selectively  
anisotropically dry etching a silicon nitride film with  
respect to a silicon oxide film, a polysilicon film and a  
silicon film. The anisotropic dry etching method  
25 according to the present invention is characterized in  
that a substrate temperature is set at  $10^\circ\text{C}$  or less, a  
mixed gas of a compound gas containing fluorine, carbon  
and hydrogen and carbon monoxide is used as a reactive gas.

Advantageously, the present invention may therefore provide  
an anisotropic dry etching method capable of selectively,  
anisotropically dry etching a silicon nitride film with  
respect to all of the silicon oxide film, the polysilicon film  
and the silicon film.

In this anisotropic dry etching method, it is preferable that the compound gas containing fluorine, carbon and hydrogen is either a mixed gas of at least one selected from the group consisting of  $\text{CHF}_3$ ,  $\text{CF}_4$  and  $\text{C}_2\text{F}_6$  and hydrogen gas or  $\text{CHF}_3$  gas. It is also preferable that a mixture ratio of  $\text{CO}$  gas to a total gas flow rate of the reactive gas is 70 to 95 volume %.

According to the present invention, there is no need to particularly use, as a compound gas containing fluorine, carbon and hydrogen, simple gas having a F to H composition ratio of 2 or less such as  $\text{CH}_2\text{F}_2$  and  $\text{CH}_3\text{F}$ . Thus, the present invention is not restricted by the F to H composition ratio.

#### BRIEF DESCRIPTION OF THE DRAWING

FIG. 1 is a graph showing the relationship between substrate temperature and etch rates of respective films to describe the principle of the present invention;

FIG. 2 is a cross-sectional view showing an embodiment of applying the present invention to contact hole etching in the order of steps; and

FIG. 3 is a cross-sectional view showing an embodiment of applying the present invention to groove etching in the order of steps.

#### DESCRIPTION OF THE PREFERRED EMBODIMENTS

Embodiments of the present invention will now be described in detail with reference to the accompanying drawings. Description will be given first to a silicon nitride film and the etch selectivity of the silicon

nitride film with respect to other silicon oxide films and silicon films. FIG. 1 is a graph showing the relationship between substrate temperature and etch rates of respective films, with the axis of abscissas indicating the substrate temperature and the axis of ordinates indicating the film etch rate. The etch rates were measured using a parallel plate dry etching system.  $\text{CHF}_3$  was used as a compound gas containing gaseous fluorine, carbon and hydrogen. Using a mixed gas of the  $\text{CHF}_3$  and CO gas as a reactive gas, etch rates of a silicon nitride film, a polysilicon film and a silicon oxide film were measured while changing the substrate temperature. At a substrate temperature of 50°C to 100°C at which etching has been conventionally conducted, the etch rate of the silicon nitride film is slow and it is impossible to selectively etch the silicon nitride film with respect to, in particular, the silicon oxide film. As the substrate temperature was decreased, the etch rate of the silicon nitride film rapidly increased. The etch rate of the polysilicon film and that of the silicon oxide film increased slightly and far slower than that of the silicon nitride film. At a substrate temperature of 50°C or less, the etch rate of the silicon nitride film increased surprisingly. At a substrate temperature of 10°C or less, the etch selectivity of the silicon nitride film with respect to the silicon oxide film was 5 or more. Further, since the etch rate of the polysilicon film was far slower, it was possible to etch the silicon nitride film with etch



selectivity of 10 or more with respect to the polysilicon film.

It is considered that the reason the etch rate of the silicon nitride film increases as the substrate temperature decreases is that the lower temperature of the substrate causes reaction products to be easily generated. In other words, when CO is added, fluorine is taken out in the form of COF and plasma lacking in fluorine occurs. Besides, as the substrate temperature decreases, the quantity of  $C_xH_y$ , which is decomposed or generated from fluorocarbon gas such as  $CHF_3$ , adhering to the substrate increases. If so, HCN which is a reaction product increases in quantity and is exhausted, with the result that the etch rate of the silicon nitride film increases.

On the other hand, when CO is added, F becomes insufficient in quantity on the silicon oxide film. As a result, the etch rate of the silicon oxide film decreases. The deposition of carbon rich polymerized films of high ion impact resistance on the polysilicon film or the silicon film, when CO is added, is accelerated. Since the deposited films protect the silicon surface, it is considered that the etch rate of the polysilicon film and that of the silicon film are slow.

As regards a mixture ratio of  $CHF_3$  to CO, it was discovered that CO gas needs to be added by 70% or more of the overall gas flow rate to generate plasma lacking in fluorine and to suppress the etch rate of the silicon oxide film to low level.

Next, description will be given to an anisotropic dry etching method embodying the present invention,

based on the principle stated above. FIGS. 2A through 2E are cross-sectional views showing the first embodiment in the order of steps. In the first embodiment, the anisotropic dry etching method embodying the present invention is employed to remove the nitride film of a self-aligned contact with the nitride film used as a stopper.

First, as shown in FIG. 2A, a silicon oxide film 2, a polysilicon film 3 and a silicon oxide film 4 are sequentially deposited on a silicon substrate 1. After forming a resist (not shown), the resist is patterned. Using the resist as a mask, the silicon oxide film 2, the polysilicon film 3 and the silicon oxide film 4 are subjected to anisotropic dry etching, thereby forming an electrode wiring.

Next, as shown in FIG. 2B, a silicon oxide film 5 is deposited on the entire surface. The silicon oxide film 5 is subjected to anisotropic etch-back thereby to form a sidewall on the side surface of the wiring.

As shown in FIG. 2C, a silicon nitride film 6 serving as an etching stopper is then deposited on the entire surface and an interlayer insulating film 7, such as a BPSG film, is formed. Thereafter, a contact hole pattern is formed with a resist 8.

Subsequently, as shown in FIG. 2D, using the resist 8 as a mask, the interlayer insulating film 7 is subjected

to an anisotropic dry etching and a contact hole 9 is formed to such an extent as to reach the silicon nitride film 6 serving as a stopper.

As shown in FIG. 2E, the silicon nitride film 6  
5 exposed to the interior of the contact hole 9 is then removed and a hole is formed in the silicon substrate 1. The embodiment of the present invention is applied to the removal of the silicon nitride film 6. Namely, the silicon nitride film 6 is selectively etched with respect  
10 to the silicon oxide film 5 and the silicon substrate 1 at the same time.

In other words, an etching is conducted, for example, for 90 seconds with CHF<sub>3</sub> gas of 10 sccm, CO gas of 90 sccm, pressure of 40 mTorr, high frequency (RF) power of 150 w  
15 and at a substrate temperature of 10°C. As a result of the etching process, the silicon nitride film 6 within the contact hole 9 is selectively removed. At this time, since the silicon nitride film can be selectively etched with respect to the silicon oxide film 5 and the silicon  
20 substrate 1 according to the present invention, the silicon oxide film 5 and the silicon substrate 1 are cut less in quantity. Thereafter, a conductive material is buried into the contact hole 9 to provide continuity between the substrate and the upper wiring layer. In this  
25 case, a contact hole can be formed in a stable manner by self-aligned contact without short-circuiting the electrode wiring with the conductive material and without the need to dig deeply the silicon substrate.

FIGS. 3A through 3C are cross-sectional views showing the second embodiment of the present invention in the order of steps, in which a method embodying the present invention is applied to the groove formation step of forming a groove wiring on a contact plug of polysilicon.

First, as shown in FIG. 3A, a contact plug 11 of polysilicon is formed in an oxide film 12 deposited on a silicon substrate 10. Thereafter, a silicon nitride film 13 serving as an etching stopper and a silicon oxide film 14 serving as an interlayer film are sequentially deposited and further a groove wiring pattern is formed with a resist 15.

As shown in FIG. 3B, using the resist 15 as a mask, an anisotropic dry etching is then conducted to the silicon oxide film 14 and a groove is formed to such an extent as to reach the silicon nitride film 13 serving as a stopper.

Thereafter, as shown in FIG. 3C, using the resist 15 as a mask, the silicon nitride film 13 is removed thereby to expose the polysilicon plug 11. The present embodiment is applied to the removal of the silicon nitride film 13. Namely, the silicon nitride film 13 is selectively etched with respect to the silicon oxide film 12 and the polysilicon plug 11 at the same time. The same etching conditions as those in the first embodiment are employed. Thus, the silicon nitride film 13 is removed. Here, since the silicon nitride film 13 can be selectively etched with respect to the silicon oxide film 12 and the polysilicon

plug 11, the silicon oxide film 12 and the polysilicon plug 11 are cut less and it is thereby possible to flatten the bottom of the groove. Accordingly, later wiring formation can be conducted in a stable manner.

5        Needless to say, the present invention should not be limited to the above-stated embodiments. In the embodiments, a mixed gas of compound gas containing carbon,

hydrogen and fluorine, and CO is used. For example, a small quantity of oxygen gas, rare gas (inert gas) or nitrogen gas may  
10       be added to the mixed gas so as to enhance etching removability.

As described so far, according to the preferred embodiments of the present invention, by setting substrate temperature at 10°C or

less and by using a mixed gas of compound gas containing fluorine, carbon and hydrogen, and carbon monoxide (CO) as  
15       reactive gas, the silicon nitride film may advantageously be selectively anisotropically dry etched with respect to all of the silicon oxide film, the polysilicon film and the silicon film. Thus, the present invention may realize a semiconductor device structure which has been  
20       conventionally difficult to realize.

CLAIMS

1. An anisotropic etching method comprising the step of selectively anisotropically dry etching a silicon nitride film with respect to a silicon oxide film, a polysilicon film and a silicon film, said etching being  
5 conducted under conditions that a substrate temperature is 10°C or less and a mixed gas of a compound gas containing fluorine, carbon and hydrogen, and carbon monoxide is used as a reactive gas.

10 2. The anisotropic dry etching method according to claim 1, wherein said compound gas containing fluorine, carbon and hydrogen is either a mixed gas of hydrogen gas and at least one selected from the group consisting of  $\text{CHF}_3$ ,  $\text{CF}_4$  and  $\text{C}_2\text{F}_6$  gas or  $\text{CHF}_3$  gas.

15 3. The anisotropic dry etching method according to claim 1, wherein a mixture ratio of CO gas to a total gas flow rate of the reactive gas is 70 to 95 volume %.

4. The anisotropic dry etching method according to claim 2, wherein a mixture ratio of CO gas to a total gas  
20 flow rate of the reactive gas is 70 to 95 volume %.

5. The anisotropic dry etching method according to claim 1, wherein a gas selected from the group consisting of oxygen gas, rare gas and nitrogen gas is added to said compound gas containing fluorine, carbon and hydrogen.

25 6. The anisotropic dry etching method according to claim 2, wherein a gas selected from the group consisting of oxygen gas, rare gas and nitrogen gas is added to said compound gas containing fluorine, carbon and hydrogen.

7. The anisotropic dry etching method according to claim 3, wherein a gas selected from the group consisting of oxygen gas, rare gas and nitrogen gas is added to said compound gas containing fluorine, carbon and hydrogen.

5        8. The anisotropic dry etching method according to claim 4, wherein a gas selected from the group consisting of oxygen gas, rare gas and nitrogen gas is added to said compound gas containing fluorine, carbon and hydrogen.

9. An etching method comprising the step of anisotropically dry etching a silicon nitride film at a substrate temperature of 10°C or less using as a reactive gas a mixture of carbon monoxide and a gas containing fluorine, carbon and hydrogen.

10. An etching method substantially as described herein with reference to the drawings.

11. A semiconductor device manufactured using an etching method as defined in any preceding claim.



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Application No: GB 9901151.2  
Claims searched: 1-11

Examiner: Graham Russell  
Date of search: 9 April 1999

## Patents Act 1977 Search Report under Section 17

### Databases searched:

UK Patent Office collections, including GB, EP, WO & US patent specifications, in:

UK CI (Ed.Q): B6J (JP, JQY)

Int CI (Ed.6): H01L

Other: Online: EPODOC, WPI

### Documents considered to be relevant:

Category	Identity of document and relevant passage	Relevant to claims
A	GB 2165992 A (SGS) see page 1 line 100	1,2,9
A	US 5643473 (HITACHI) see column 7 lines 9-16	1,2,9
A	US 4529476 (SHOWA) see column 1 lines 40-50	1,9

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Y	Document indicating lack of inventive step if combined with one or more other documents of same category.	P	Document published on or after the declared priority date but before the filing date of this invention.
&	Member of the same patent family	E	Patent document published on or after, but with priority date earlier than, the filing date of this application.